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D. Scott
4-16-03

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Defect Related Reliability
Failures in CMOS SRMAS
First Named Inventor: Surya Bhattacharya
Domestic/Foreign Application: Domestic Application
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Disclosure Statement

Application Number: 10/020,208
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System and Method to Screen Defect Related Reliability Failures in CMOS SRMAS

First Named Inventor: Surya Bhattacharya

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us-information-disclosure-statement

IDS18750330001ids.xml

Comments:



Electronic Information Disclosure Statement

System and Method to Screen Defect Related Reliability Failures in CMOS SRMAS

Application:



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Applicant(s):

Surya Bhattacharya

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US Patent Documents

Note: Applicant is not required to submit a paper copy of cited US Patent Documents

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P06	6038181	2000-03-14		Braceras et al.	365/201

Signature

Examiner Name	Date
<i>Lae Sukwong</i>	<i>5/03</i>